

FIG. 1

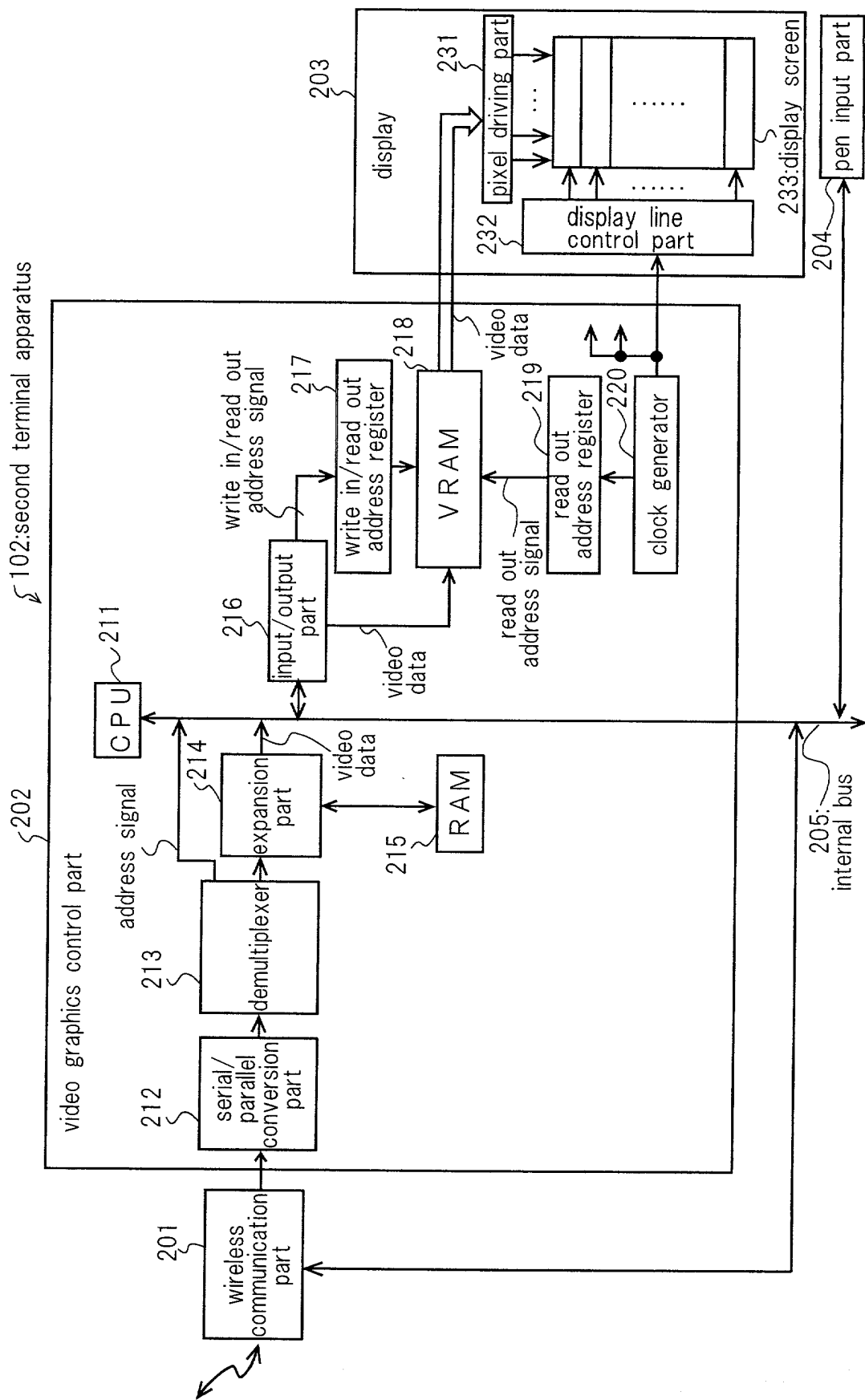


FIG. 2

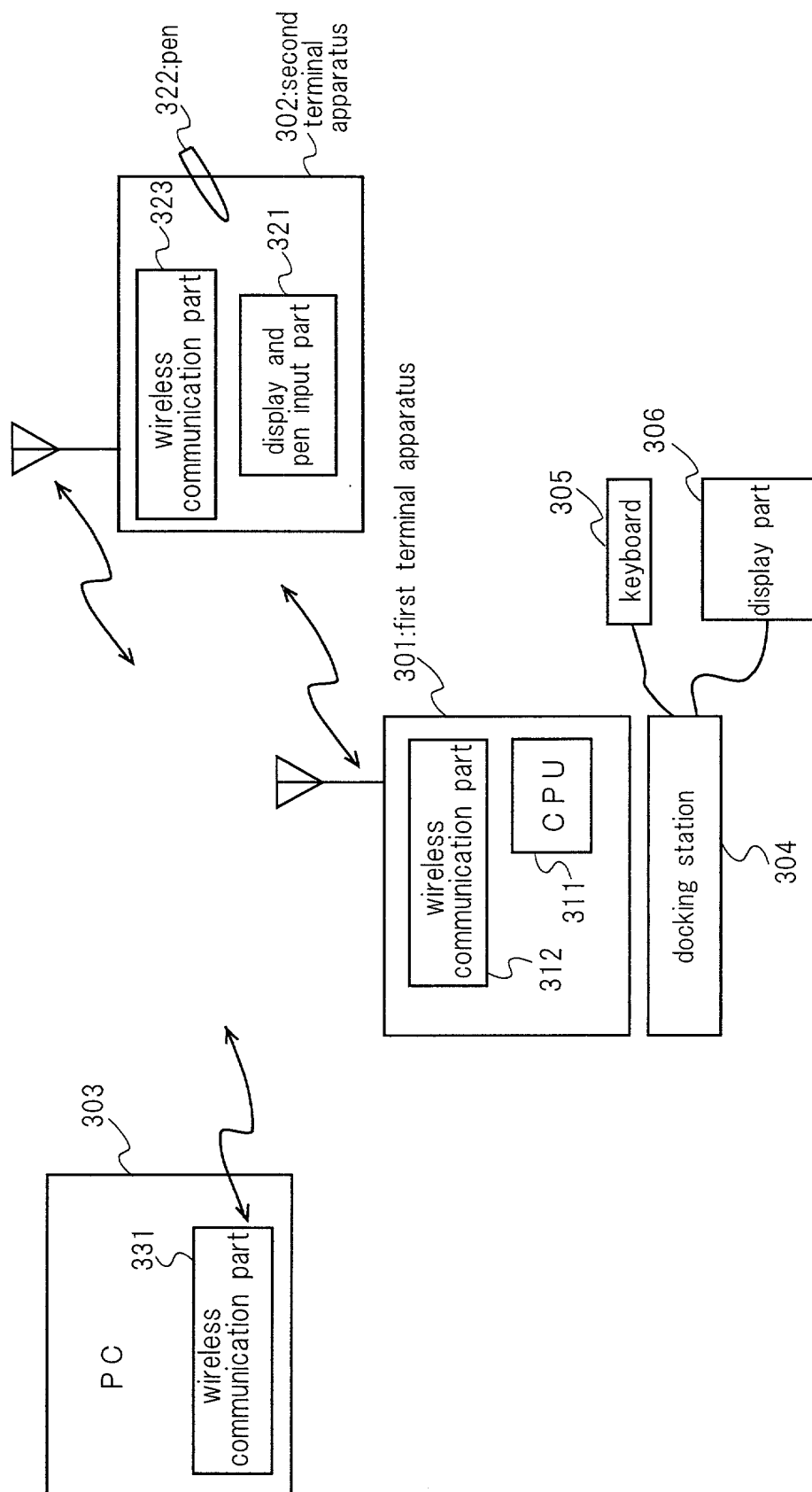


FIG. 3

FIG. 4

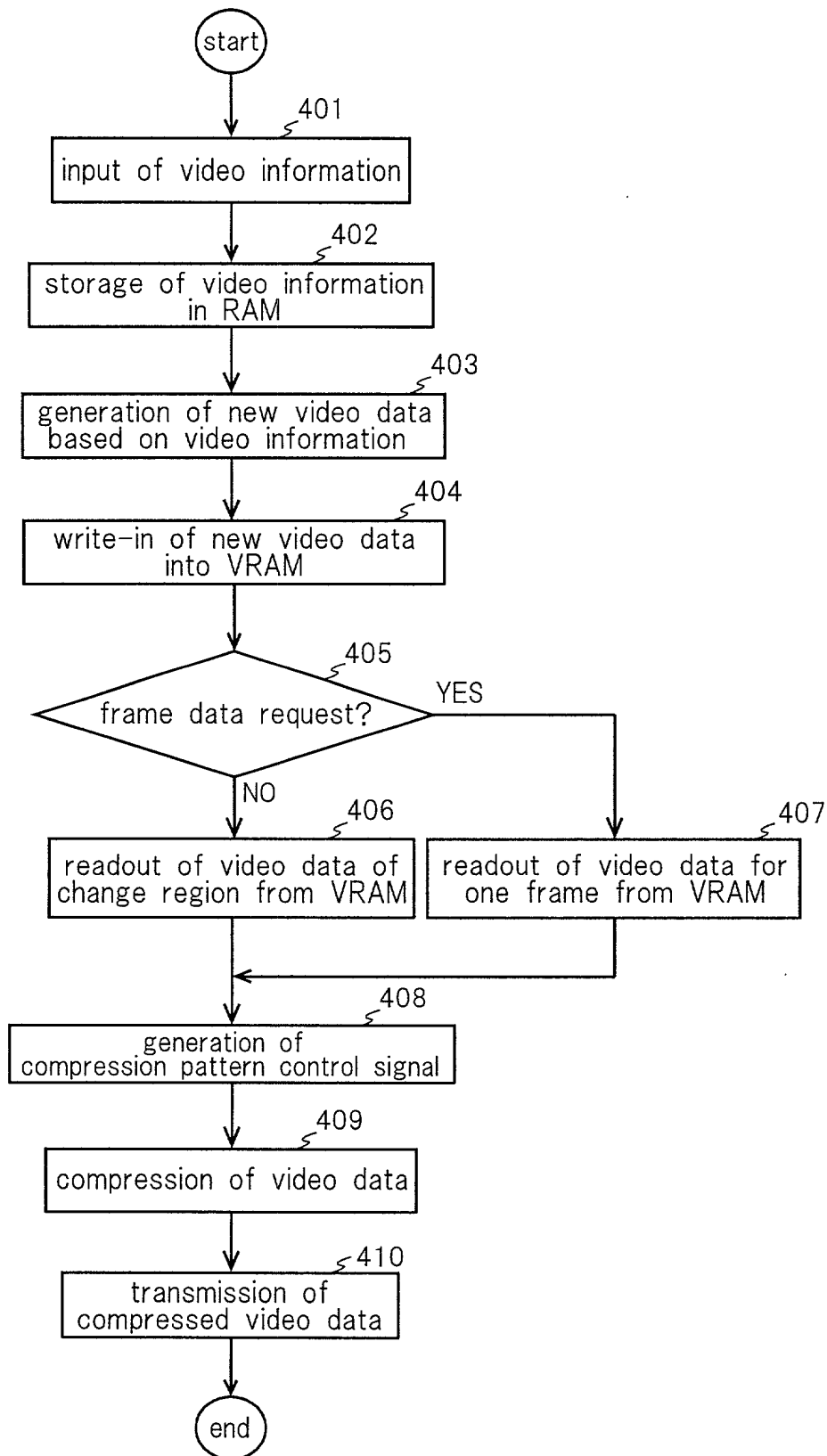
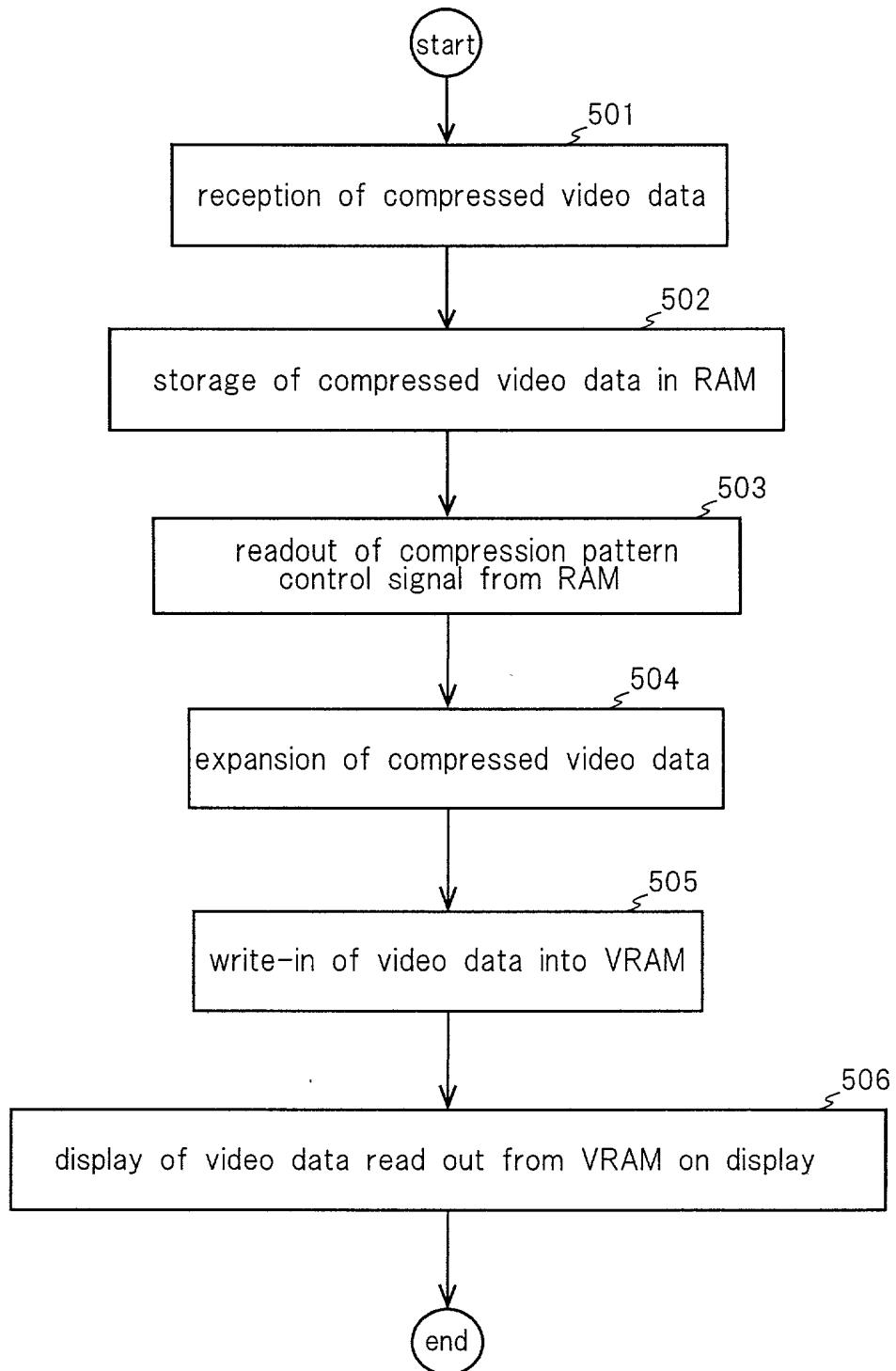


FIG. 5



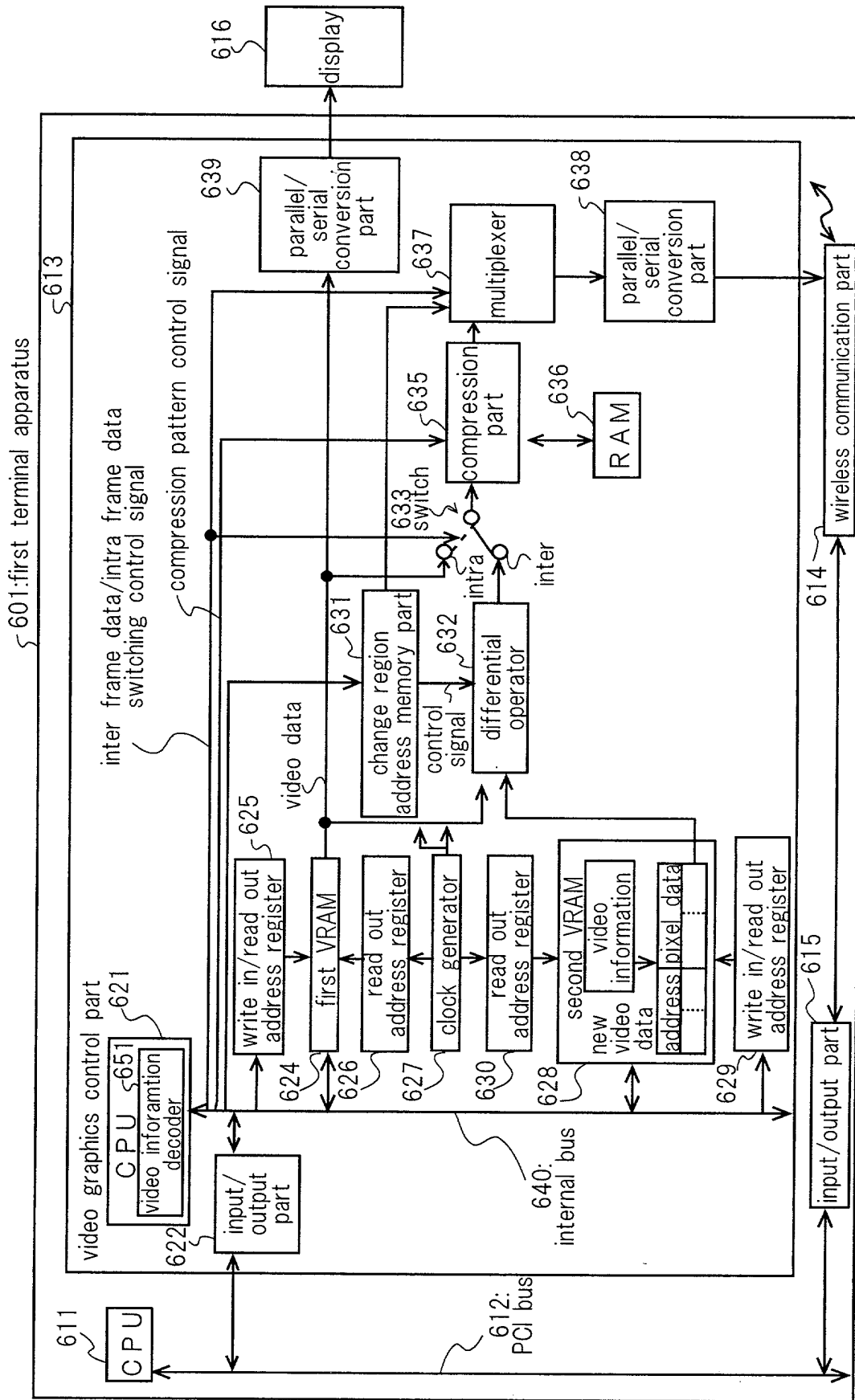


FIG. 6

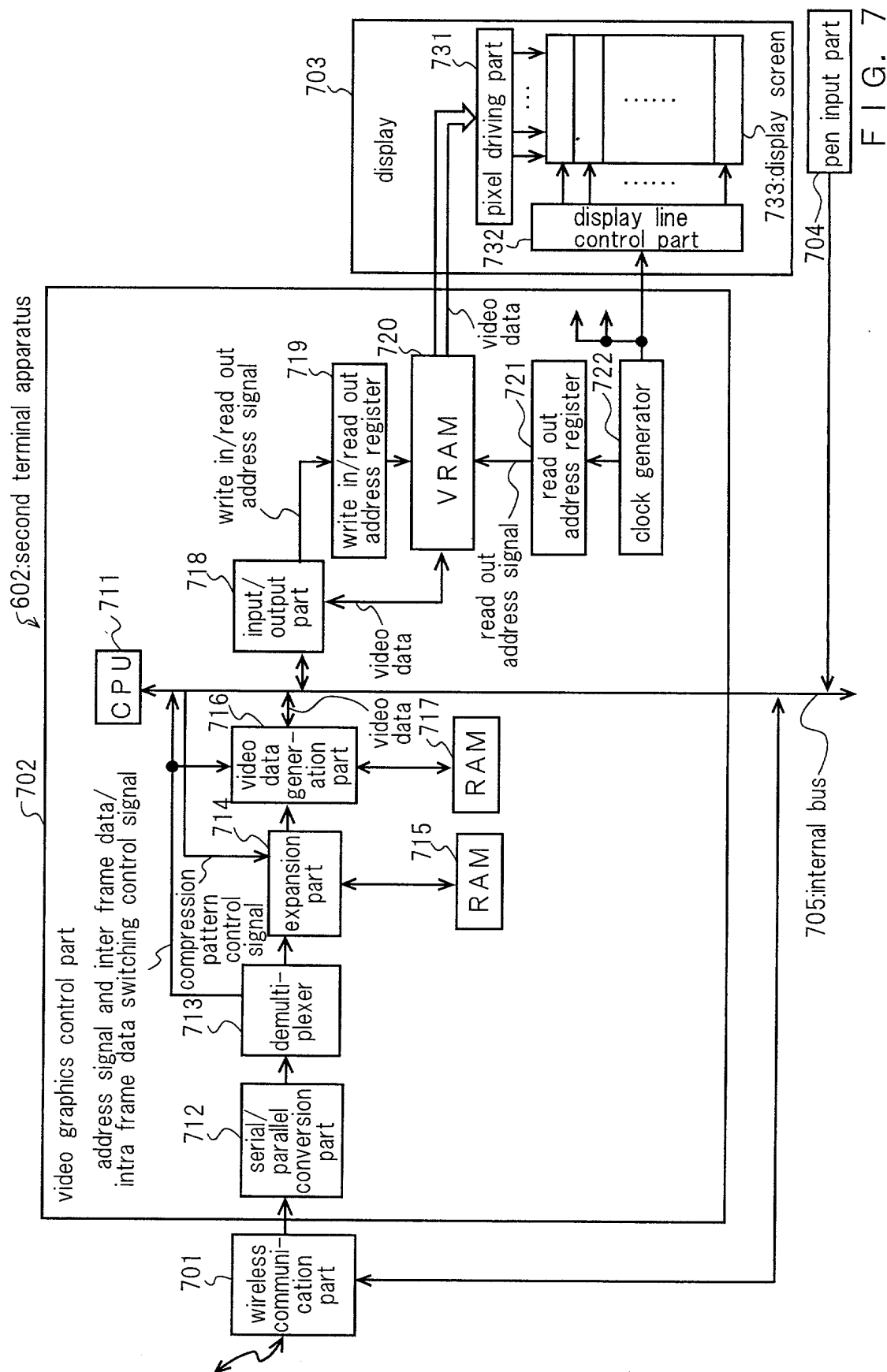


FIG. 8

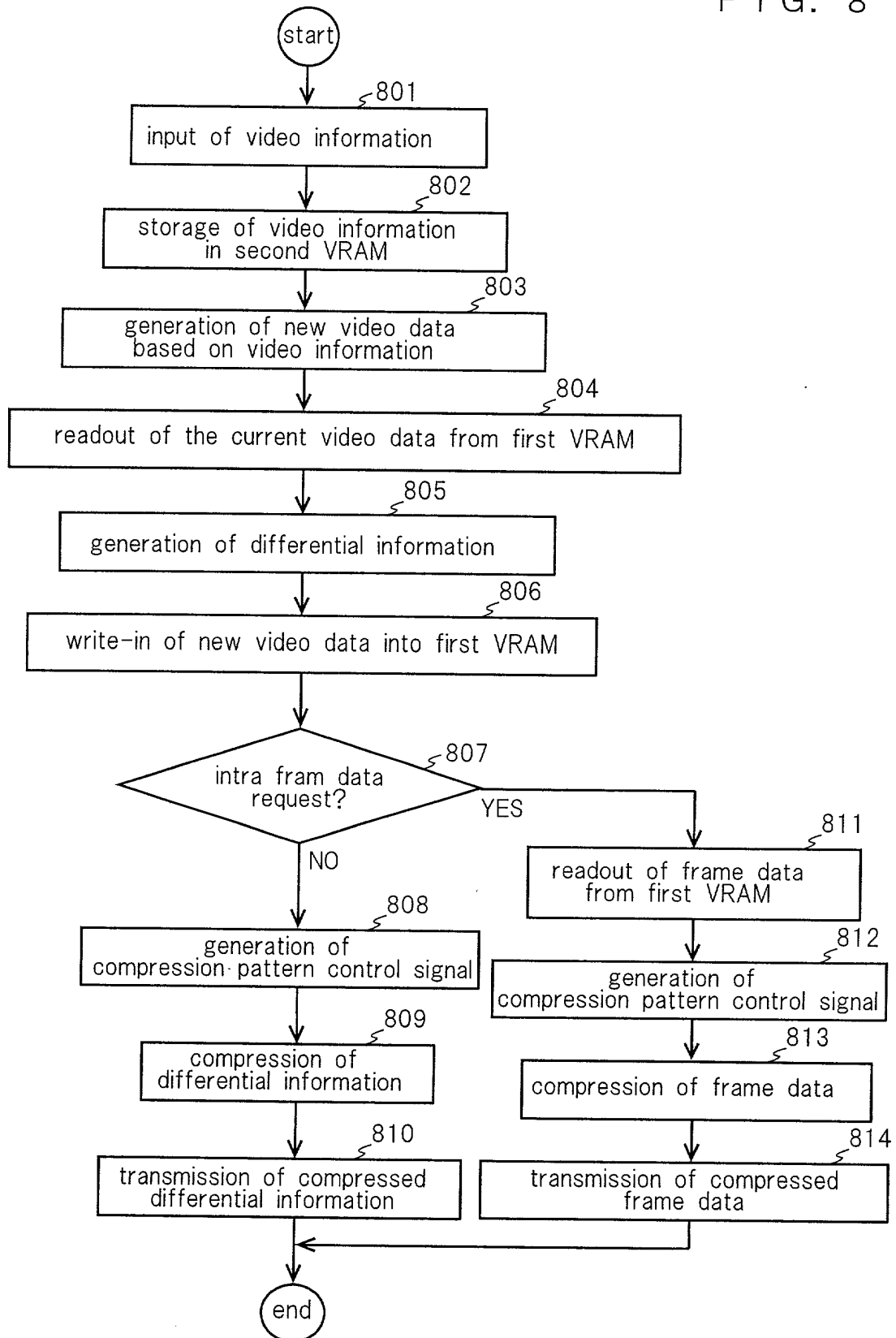


FIG. 9

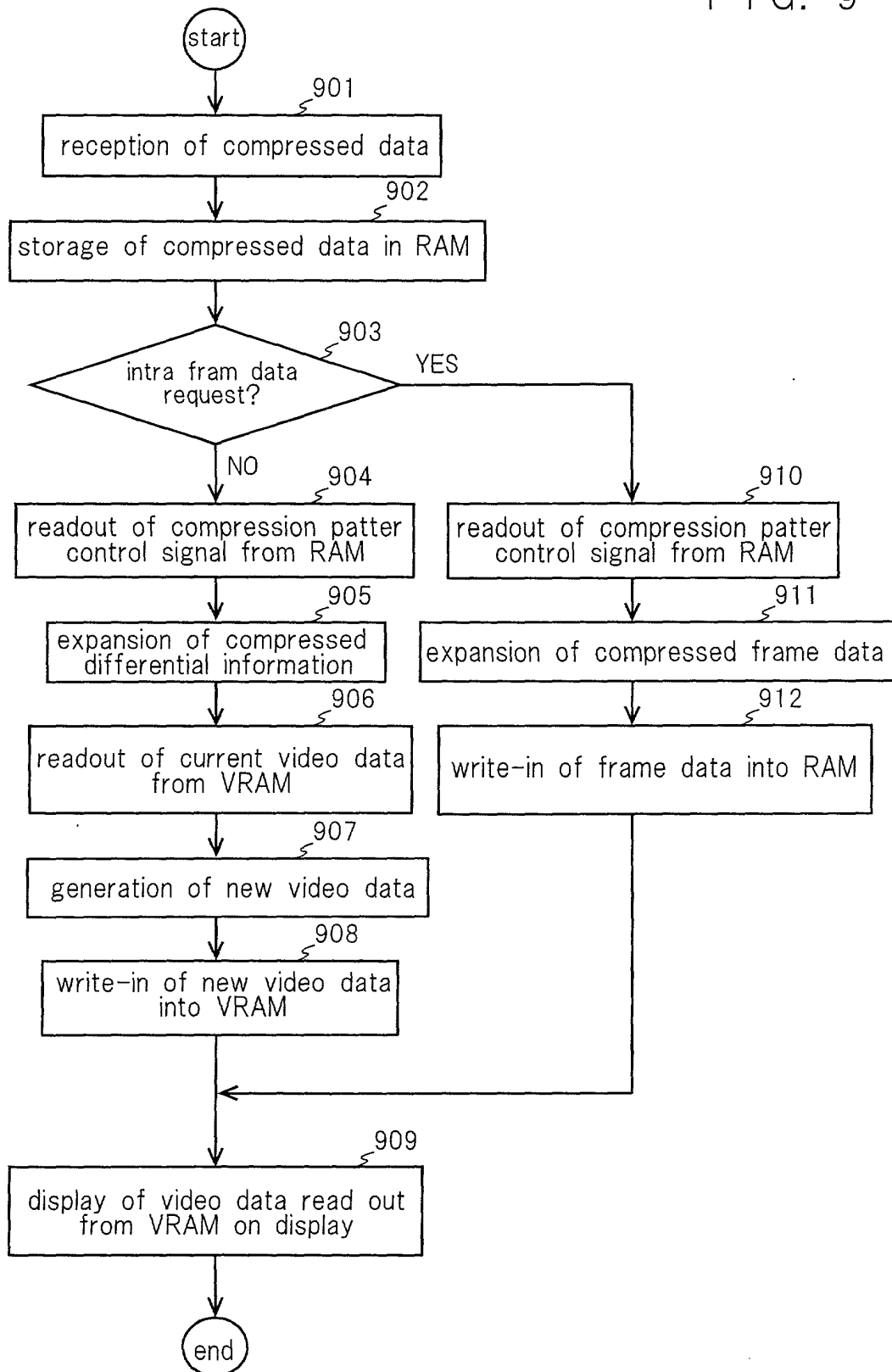


FIG. 10

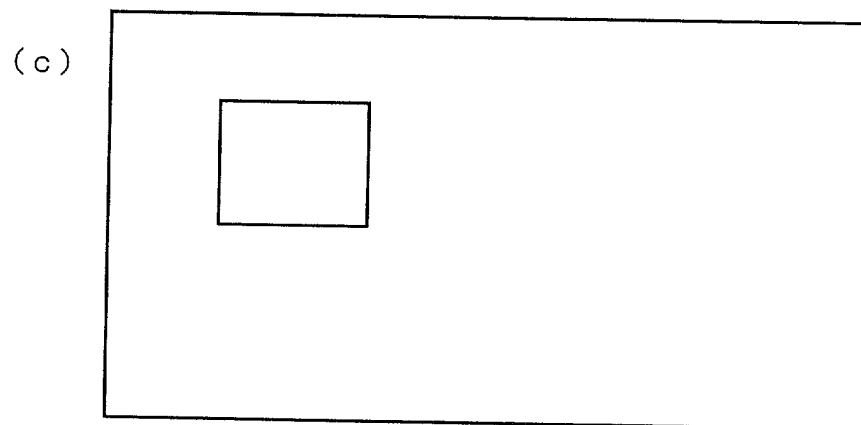
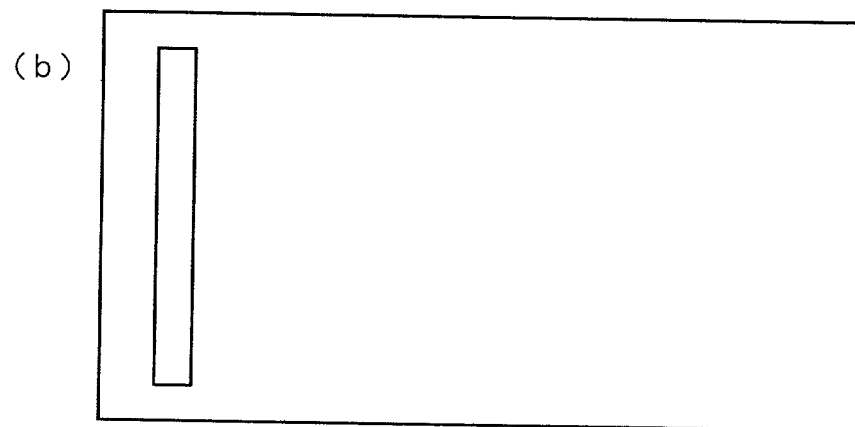
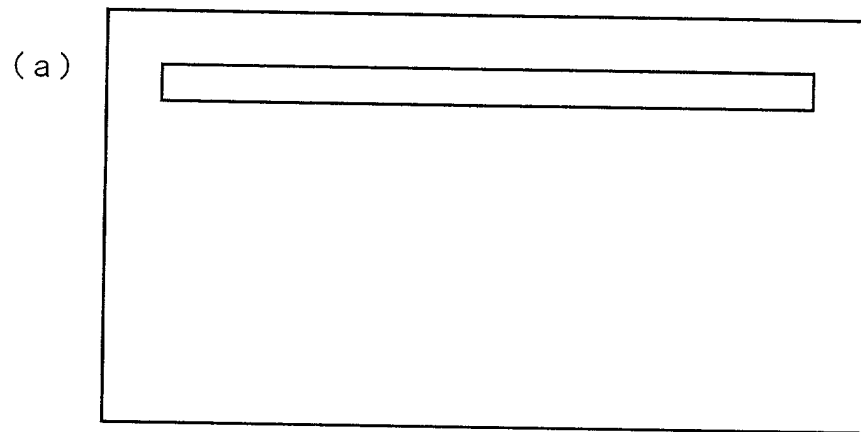


FIG. 11

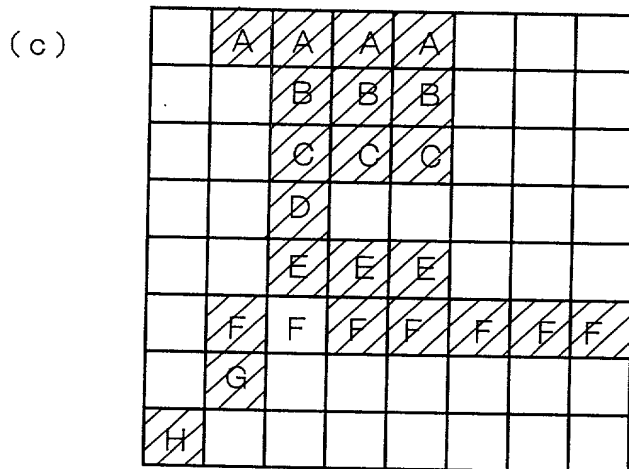
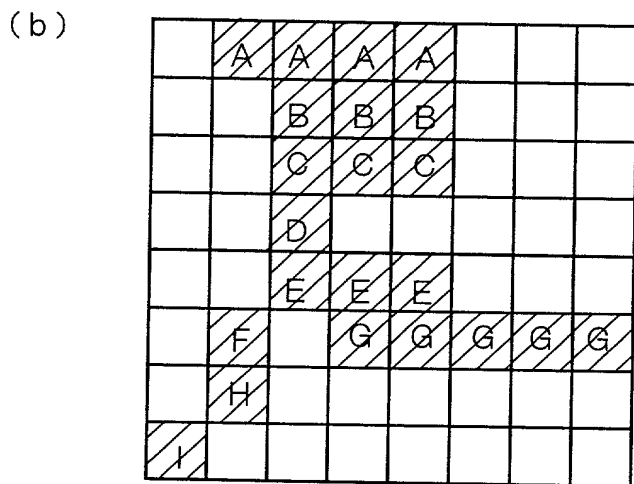
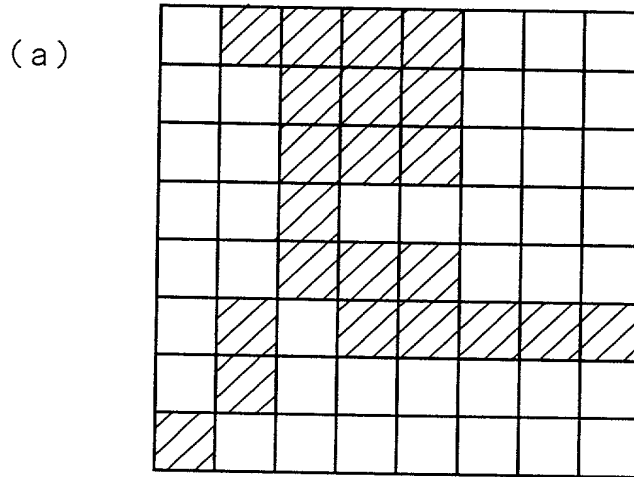


FIG. 12

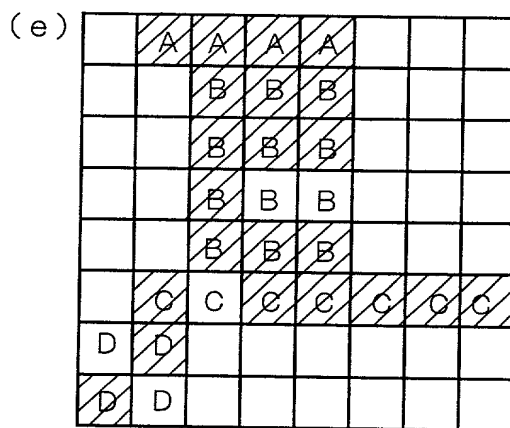
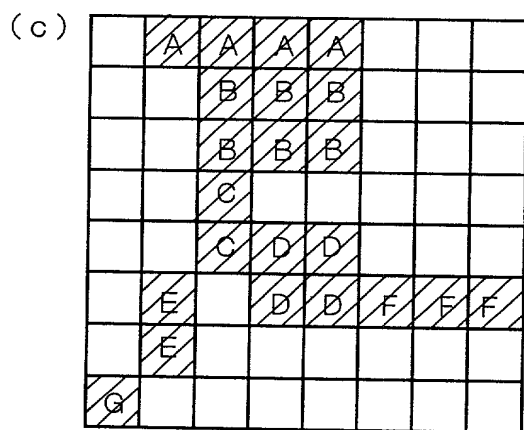
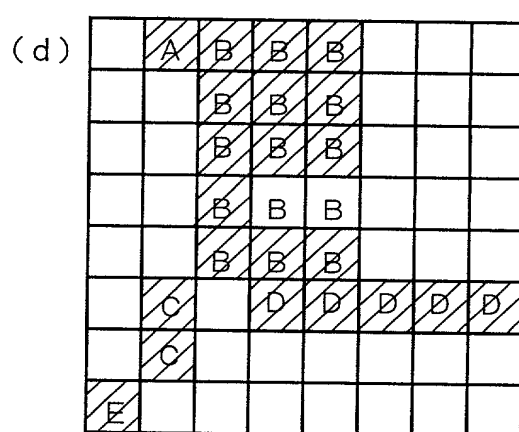
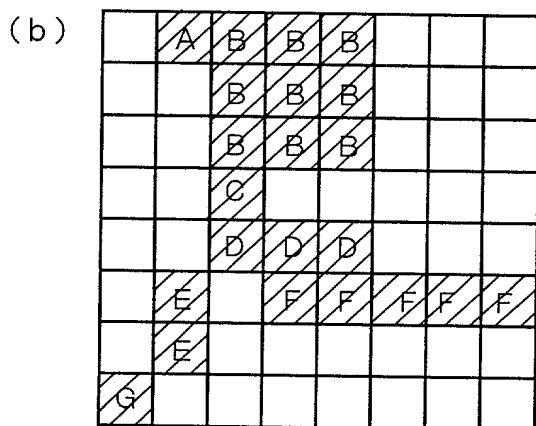
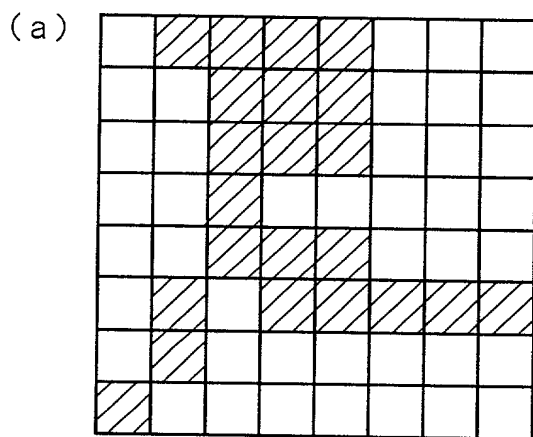


FIG. 13

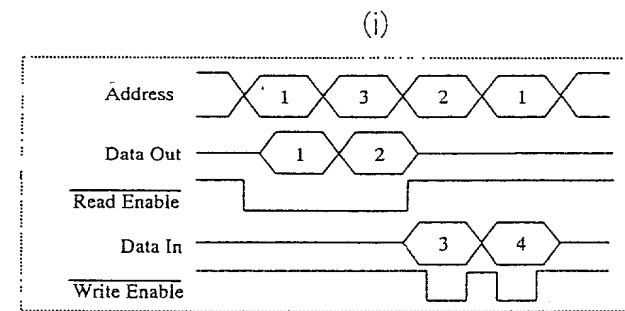
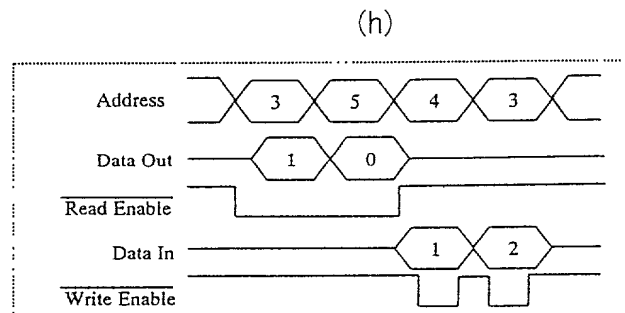
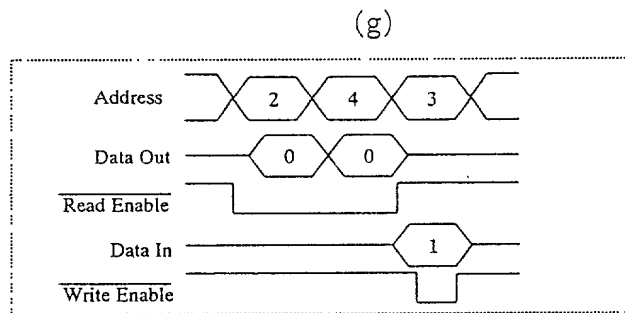
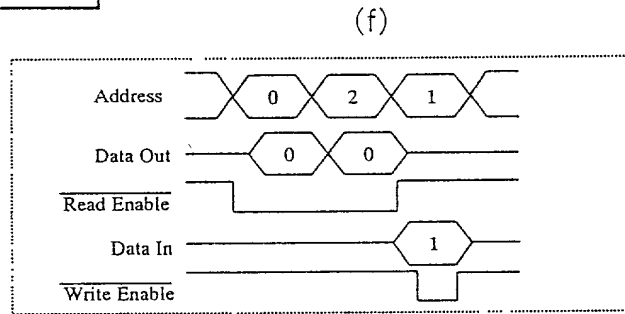
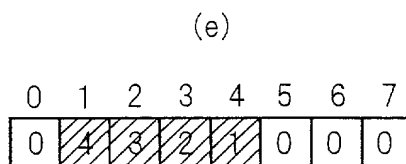
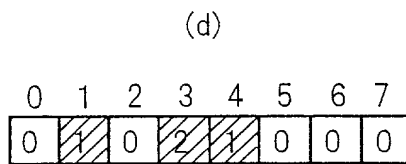
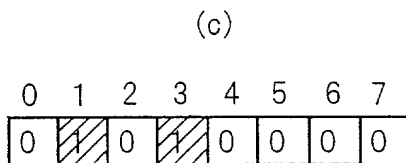
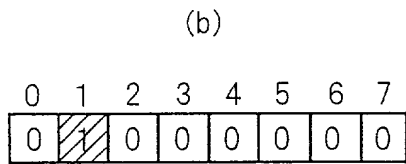
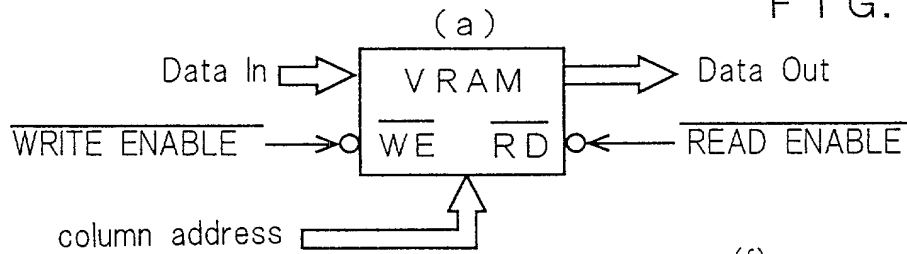


FIG. 14

		row address							
		0	1	2	3	4	5	6	7
column address	0		(4,1)	(3,5)	(2,3)	(1,3)			
	1			(3,4)	(2,2)	(1,2)			
	2			(3,3)	(2,1)	(1,1)			
	3			(1,2)					
	4			(3,1)	(2,2)	(1,2)			
	5		(1,2)		(5,1)	(4,1)	(3,1)	(2,1)	(1,1)
	6		(1,1)						
	7	(1,1)							

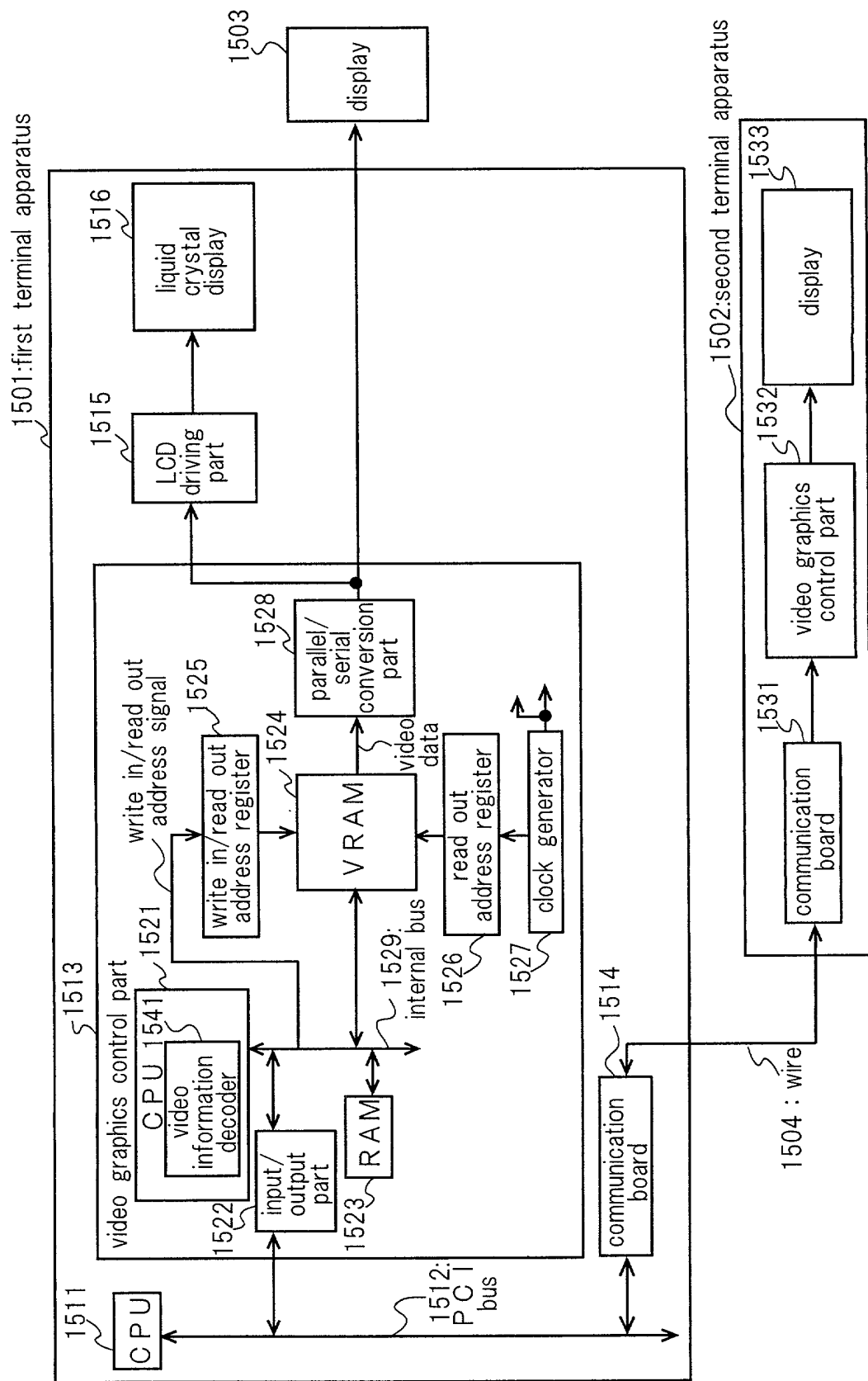


FIG. 15